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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brian L. Schmidt et al.

Title:

METHOD FOR INTERCONNECTING ANODES AND CATHODES IN A FLAT CAPACITOR

Docket No.: 279.339US3

Serial No.: 10/804,288

Filed: March 18, 2004

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2813

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ A Communication Concerning Related Applications (2 pgs.).
- ☒ A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (2 pgs), and copies of 2 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

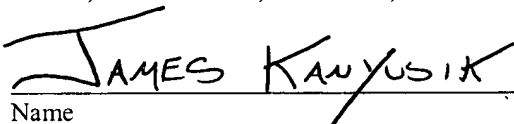
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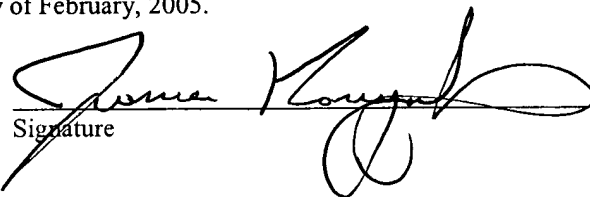
By: 

Atty: Peter C. Maki

Reg. No. 42,832

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24th day of February, 2005.


Name


Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

(GENERAL)

S/N 10/804,288

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brian L. Schmidt et al. Examiner: Unknown
Serial No.: 10/804,288 Group Art Unit: 2813
Filed: March 18, 2004 Docket: 279.339US3
Title: METHOD FOR INTERCONNECTING ANODES AND CATHODES IN A FLAT
CAPACITOR

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No :10/804,288

Filing Date: March 18, 2004

Title: METHOD FOR INTERCONNECTING ANODES AND CATHODES IN A FLAT CAPACITOR

Page 2

Dkt: 279.339US3

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

BRIAN L. SCHMIDT ET AL.

By their Representatives,

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(612) 359-3267

Date 2/23/05

By Peter C Maki
Peter C. Maki
Reg. No. 42,832

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JAMES KANYSIK
Name

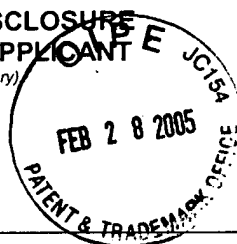
[Signature]
Signature

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number 10/804,288**Filing Date** March 18, 2004**First Named Inventor** Schmidt, Brian**Group Art Unit** 2813**Examiner Name** Unknown

Sheet 1 of 2

Attorney Docket No: 279.339US3**US PATENT DOCUMENTS**

| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Filing Date If Appropriate |
|--------------------|---------------------|------------------|---|----------------------------|
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EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

Substitute for form 1449A/PTO
**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Use as many sheets as necessary)

Complete if Known

| | |
|-----------------------------|----------------|
| Application Number | 10/804,288 |
| Filing Date | March 18, 2004 |
| First Named Inventor | Schmidt, Brian |
| Group Art Unit | 2813 |
| Examiner Name | Unknown |

Sheet 2 of 2

Attorney Docket No: 279.339US3

| | | | | |
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| | US-5,634,938 | 06/03/1997 | Swanson, David K., et al. | 10/25/1995 |
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

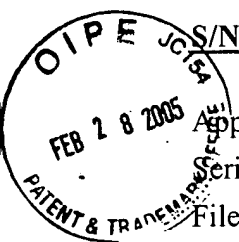
| Examiner Initials* | Cite No ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
|-----------------------|-------------------------|---|----------------|
| | | DOFFING, B. , et al., "METHOD AND APPARATUS FOR PROVIDING FLEXIBLE PARTIALLY ETCHED CAPACITOR ELECTRODE INTERCONNECT", US Patent Application Ser. No. 10/996,903, filed November 24, 2004 (Attorney Docket No. 279.831US1), 239 Pgs. | |
| | | O'PHELAN, MICHAEL J., "FLAT CAPACITOR HAVING AN ACTIVE CASE", Application Serial No. 10/969,441, Filed October 20, 2004, Attorney Docket No. 279.235US2, 44 pgs. | |

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached



S/N 10/804,288

PATENT

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Examiner: Unknown

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Title: METHOD FOR INTERCONNECTING ANODES AND CATHODES IN A
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COMMUNICATION CONCERNING RELATED APPLICATIONS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

| <u>Serial/Patent No.</u> | <u>Filing Date</u> | <u>Attorney Docket</u> | <u>Title</u> |
|--------------------------|----------------------|------------------------|--|
| 10/299,234 6,709,946 | November 19, 2002 | 279.339US2 | METHOD FOR INTERCONNECTING ANODES AND CATHODES IN A FLAT CAPACITOR |
| 10/413,680 | April 15, 2003 | 279.268US2 | CONFIGURATIONS AND METHODS FOR MAKING CAPACITOR CONNECTIONS |
| 10/874,798 | June 23, 2004 | 279.823US1 | METHOD AND APPARATUS FOR PROVIDING FLEXIBLE PARTIALLY ETCHED CAPACITOR ELECTRODE INTERCONNECT |
| 10/882,144 | June 30, 2004 | 279.169US3 | METHOD OF CONSTRUCTING A CAPACITOR STACK FOR A FLAT CAPACITOR |
| 10/969,441 | October 20, 2004 | 279.235US2 | FLAT CAPACITOR HAVING AN ACTIVE CASE |

COMMUNICATION CONCERNING RELATED APPLICATIONS

Serial Number: 10/804,288

Filing Date: March 18, 2004

Title: METHOD FOR INTERCONNECTING ANODES AND CATHODES IN A FLAT CAPACITOR

Page 2

Dkt: 279.339US3

10/996,903

November
24, 2004

279.831US1

METHOD AND APPARATUS FOR
PROVIDING FLEXIBLE PARTIALLY
ETCHED CAPACITOR ELECTRODE

Respectfully submitted,

BRIAN L. SCHMIDT ET AL.

By Applicants' Representatives,

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Date 2/24/05 By Peter C Maki
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JAMES KANYOSIK
Name

[Signature]
Signature